

REMARKS

The Examiner is thanked for the indication of allowability with respect to claims 1-3 and 6-8. However, Claims 5 and 9-14 were rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted art (Japanese Laid Open Application 11-281957 of figures 1-4) in view of Su et al. (US 6,590,555).

Claim 14 has been amended to fix a minor editorial error.

I. Response to Rejections Under 35 U.S.C. 103(a)

The prior art (Japanese Laid Open Application 11-281957) discloses a device for eliminating the flickering of thin-film transistor liquid-crystal-display (TFT-LCD), the device comprising a first switch (SW1), configured between a power supply and an output end of the device, a discharge circuit, with one end connected between the first switch and the output end of the device and the other end connected to the ground, a second switch for controlling discharge circuit grounding; a trigger signal source for controlling the switches, wherein the first switch is on and the second switch is off, a output end of the device is connected to the power supply and the circuit is recharged, and when the first switch is off and the second switch is on, the discharge circuit is grounded and discharged. However, it is not understood where the prior art discloses "means for delaying opening of the second switch at lower temperatures."

Su et al. (US 6,590,555) disclose a driving circuit including a switch circuit and a temperature sensor. The temperature sensor detects the temperature for generating a selection signal to select either the first polar control signal at low temperature or the second polar control signal at high temperature to output. However, there is no "delay characteristic" associated with the temperature described for the driving circuit. Therefore, this reference does not disclose "means for delaying opening of the second switch at lower temperatures", as recited by claim 5. Also, what is the connection between the claimed "second switch" and the switch circuit mentioned at column 4, line 34 of Su et al? The Examiner seems to equate the two in the official action without any analysis as to why one is the equivalent of the other. In short, modifying the Japanese

Laid Open document based on Su et al does not lead to the invention of claim 5.

Since independent claims 9 and 14 recite somewhat similar limitations, namely "a thermistor for delaying opening of the second transistor at lower temperatures" (claim 9) and "means for delaying opening of the second switch at lower temperatures" (claim 14), claims 9 and 14 should also not be rejected under 35 U.S.C. 103(a) as being unpatentable over Japanese Laid Open Application 11-281957 of figures 1-4 in view of Su et al. (US 6,590,555) since the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would not have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Also, because claims 10-13 depend from allowable independent claim 9, there is no doubt about their patentability. Based on the above analysis, claims 5, 9-14 should be in condition for allowance. Applicant submits that this application is now in condition for allowance. Reconsideration and prompt issuance of a Notice of Allowance is earnestly solicited.

The Commissioner is authorized to charge any additional fees which may be required or credit overpayment to deposit account no. 12-0415. In particular, if this response is not timely filed, then the Commissioner is authorized to treat this response as including a petition to extend the time period pursuant to 37 CFR 1.136 (a) requesting an extension of time of the number of months necessary to make this response timely filed and the petition fee due in connection therewith may be charged to deposit account no. 12-0415.

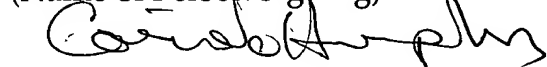
I hereby certify that this correspondence is being deposited with the United States Post Office with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

December 9, 2004

(Date of Deposit)

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December 9, 2004

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Respectfully submitted,



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